



Vincotech

30-EQ14NIA560H701-PS59F86T

datasheet

flowNPC E3BP

1400 V / 560 A

Topology features

- Kelvin Emitter for improved switching performance
- Temperature sensor
- Neutral Point Clamped Topology (I-Type)

Component features

- High speed switching
- Low collector emitter saturation voltage
- Low turn-off losses
- Optimized for hard switching topologies
- Positive temperature coefficient

Housing features

- Base isolation: Si_3N_4
- Cu baseplate
- Convex shaped baseplate for superior thermal contact
- CTI600 housing material
- Baseplate with rough surface
- Thermo-mechanical push-and-pull force relief
- Press-fit pin
- Reliable cold welding connection

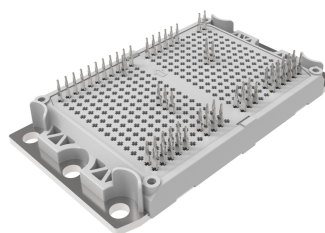
Target applications

- Solar Inverters

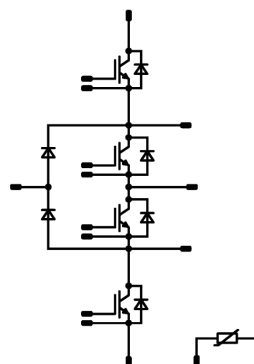
Types

- 30-EQ14NIA560H701-PS59F86T

flow E3BP 15 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
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Buck Switch

Collector-emitter voltage	V_{CES}		1400	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	398	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	1680	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	808	W
Gate-emitter voltage	V_{GES}		± 25	V
Maximum junction temperature	T_{jmax}		175	°C

Buck Diode

Peak repetitive reverse voltage	V_{RRM}		1400	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	244	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	1422	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 10\text{ ms}$ $T_j = 25\text{ °C}$	2700	A
Surge current capability	I^2t		36432	A ² s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	475	W
Maximum junction temperature	T_{jmax}		175	°C

Boost Switch

Collector-emitter voltage	V_{CES}		1400	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	398	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	1680	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	808	W
Gate-emitter voltage	V_{GES}		± 25	V
Maximum junction temperature	T_{jmax}		175	°C



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datasheet

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Boost Diode				
Peak repetitive reverse voltage	V_{RRM}		1400	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	180	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	900	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	421	W
Maximum junction temperature	T_{jmax}		175	°C

Boost Sw. Inv. Diode

Peak repetitive reverse voltage	V_{RRM}		1400	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	166	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	840	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	358	W
Maximum junction temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	7000	V
Creepage distance			17,02	mm
Clearance			12,81	mm
Comparative Tracking Index	CTI		≥ 600	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Buck Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,00896	25	4,8	5,6	6,2	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		560	25 125 150		1,99 2,21 2,27	2,35 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1400		25			22,4	µA
Gate-emitter leakage current	I_{GES}		20	0		25			400	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	$f = 100 \text{ kHz}$	0	25		25		72800		pF
Output capacitance	C_{oes}							860		pF
Reverse transfer capacitance	C_{res}							328		pF
Gate charge	Q_g	$V_{CC} = 1120 \text{ V}$	0/15		560	25		4160		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						0,12		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 2 \text{ } \Omega$ $R_{goff} = 2 \text{ } \Omega$	-5/15	800	420	25 125 150		200,79 199,93 200,08		ns
Rise time	t_r					25 125 150		27,68 28,6 29,08		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		585,27 645,49 665,63		ns
Fall time	t_f					25 125 150		52,87 112,27 131,52		ns
Turn-on energy (per pulse)	E_{on}	$Q_{tFWD}=2,31 \text{ } \mu\text{C}$ $Q_{tFWD}=2,4 \text{ } \mu\text{C}$ $Q_{tFWD}=2,41 \text{ } \mu\text{C}$				25 125 150		15,92 15,68 16,17		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		19,64 33,97 38,97		mWs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Buck Diode

Static

Forward voltage	V_F				240	25 125 150		1,41 1,74 1,88	1,6 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1400$ V				25		30	960	µA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,2		K/W
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Dynamic

Peak recovery current	I_{RM}	$di/dt=16934$ A/µs $di/dt=13876$ A/µs $di/dt=13306$ A/µs	-5/15	800	420	25 125 150		170,96 185,89 188,75		A
Reverse recovery time	t_{rr}					25 125 150		23,25 22,29 22,02		ns
Recovered charge	Q_r					25 125 150		2,31 2,4 2,41		µC
Reverse recovered energy	E_{rec}					25 125 150		0,745 0,826 0,828		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		23063,79 21703,88 23723,16		A/µs



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datasheet

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Boost Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,00896	25	4,8	5,6	6,2	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		560	25 125 150		1,99 2,21 2,27	2,35 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1400		25			22,4	µA
Gate-emitter leakage current	I_{GES}		20	0		25			400	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	$f = 100 \text{ kHz}$	0	25		25		72800		pF
Output capacitance	C_{oes}							860		pF
Reverse transfer capacitance	C_{res}							328		pF
Gate charge	Q_g	$V_{CC} = 1120 \text{ V}$	0/15		560	25		4160		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						0,12		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 2 \text{ } \Omega$ $R_{goff} = 2 \text{ } \Omega$	-5/15	800	420	25 125 150		203,65 201,23 200,74		ns
Rise time	t_r					25 125 150		29,63 31,52 31,55		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		582,04 644,45 662,51		ns
Fall time	t_f					25 125 150		41,13 94,12 108,9		ns
Turn-on energy (per pulse)	E_{on}					25 125 150		21,67 23,13 23,75		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		23,18 37,45 42,17		mWs



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datasheet

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Boost Diode

Static

Forward voltage	V_F				300	25 125 150		3,38 3,15 3,04	3,3 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1400$ V				25			1,8	µA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,23		K/W
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Dynamic

Peak recovery current	I_{RM}	$di/dt=15473$ A/µs $di/dt=15046$ A/µs $di/dt=13588$ A/µs	-5/15	800	420	25 125 150		302,74 428,3 461,5		A
Reverse recovery time	t_{rr}					25 125 150		161,76 258,07 296,09		ns
Recovered charge	Q_r					25 125 150		12,98 28,34 34,03		µC
Reverse recovered energy	E_{rec}					25 125 150		5,55 13,49 16,46		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		14064,71 10424,62 9335,54		A/µs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]		Min	Typ	Max	

Boost Sw. Inv. Diode

Static

Forward voltage	V_F			280	25 125 150			2,97 2,84 2,78	3,3 ⁽¹⁾	V
Reverse leakage current	I_R	$V_i = 1400$ V			25				1,2	µA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,27		K/W
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Thermistor

Static

Rated resistance	R				25			22		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 1484$ Ω			100	-5			5	%
Power dissipation	P				25			130		mW
Power dissipation constant	d				25			1,5		mW/K
B-value	$B_{(25/50)}$	Tol. ± 1 %						3962		K
B-value	$B_{(25/100)}$	Tol. ± 1 %						4000		K
Vincotech Thermistor Reference									I	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.



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Buck Switch Characteristics

figure 1. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

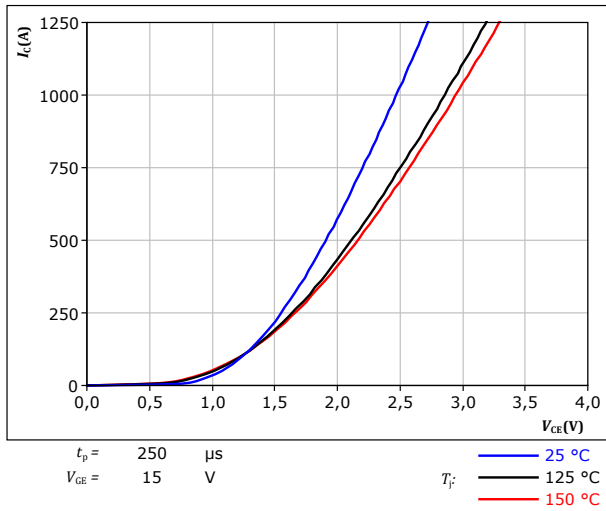


figure 2. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

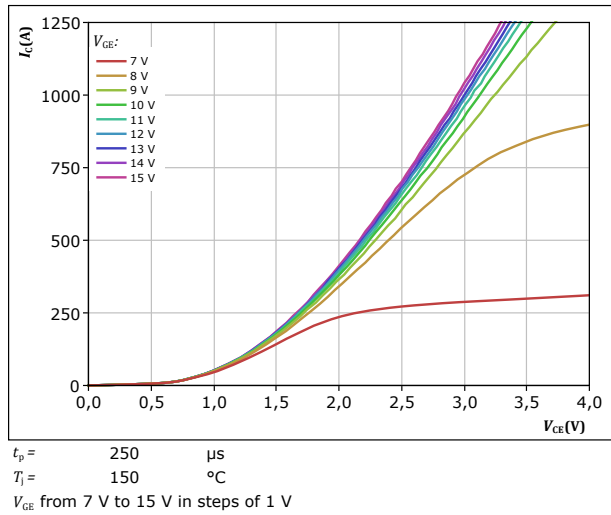


figure 3. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

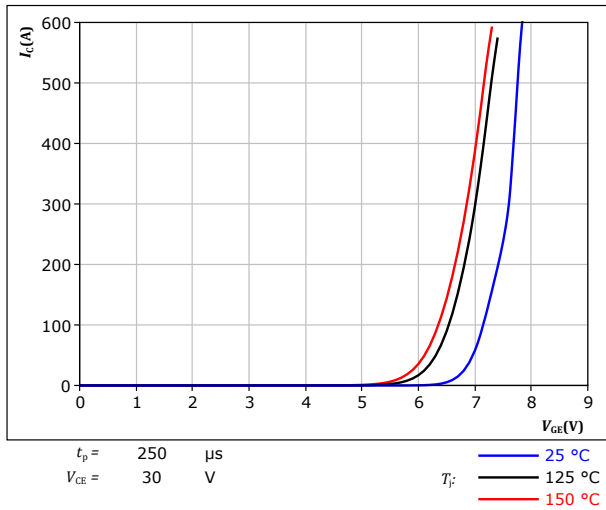
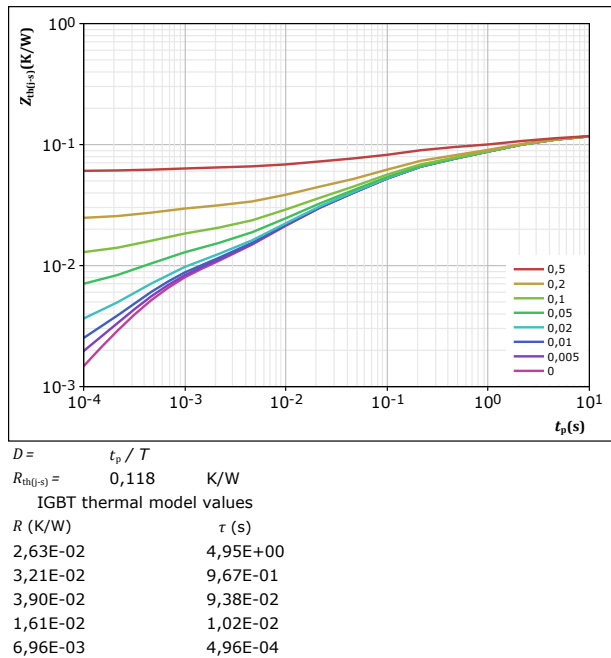


figure 4. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$





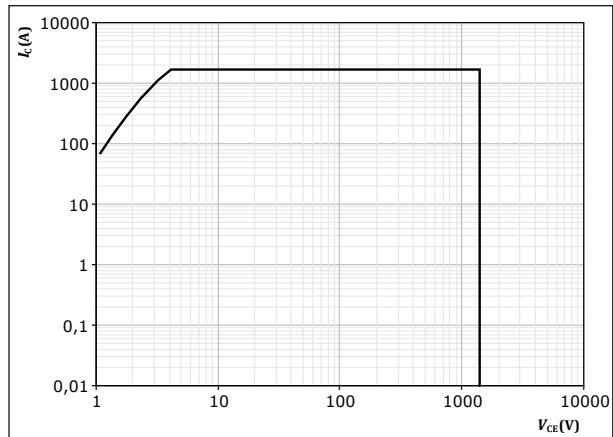
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Buck Switch Characteristics

figure 5. IGBT

Safe operating area

$$I_C = f(V_{CE})$$

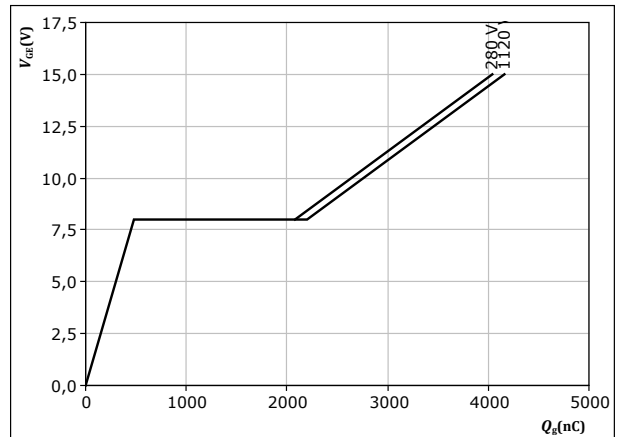


$D =$ single pulse
 $T_s = 80$ °C
 $V_{GE} = 15$ V
 $T_j = T_{jmax}$

figure 6. IGBT

Gate voltage vs gate charge

$$V_{GE} = f(Q_g)$$



$I_C = 400$ A
 $T_j = 25$ °C



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Buck Diode Characteristics

figure 7.

FWD

Typical forward characteristics

$$I_F = f(V_F)$$

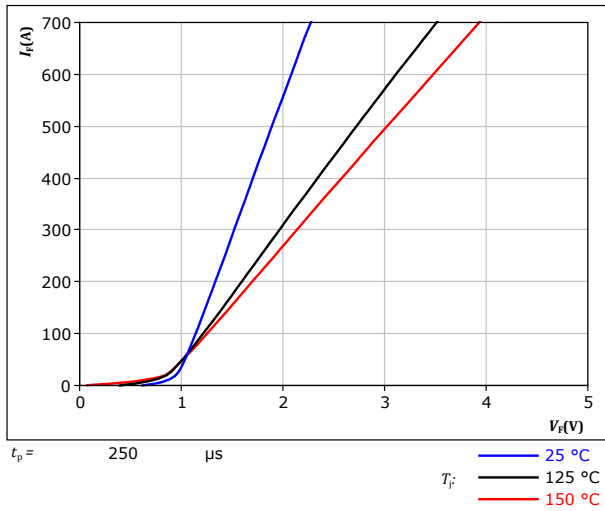
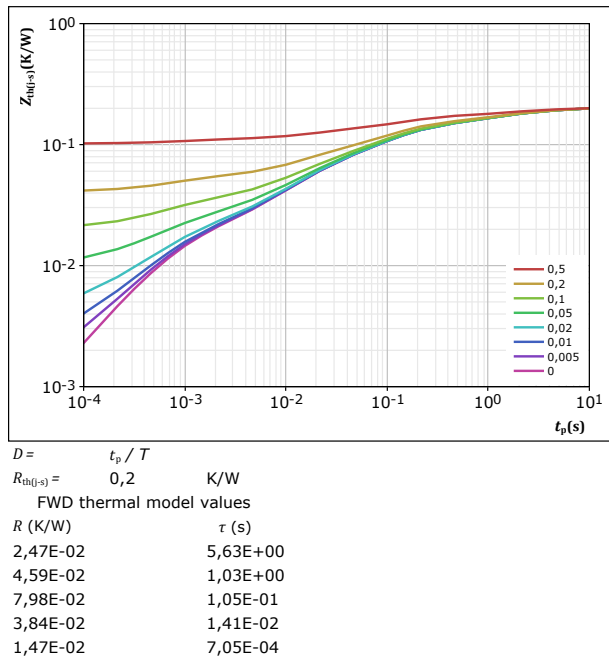


figure 8.

FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$





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Boost Switch Characteristics

figure 9. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

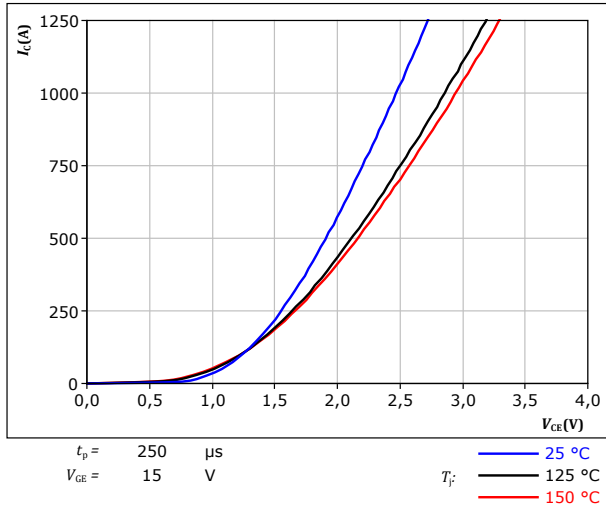


figure 10. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

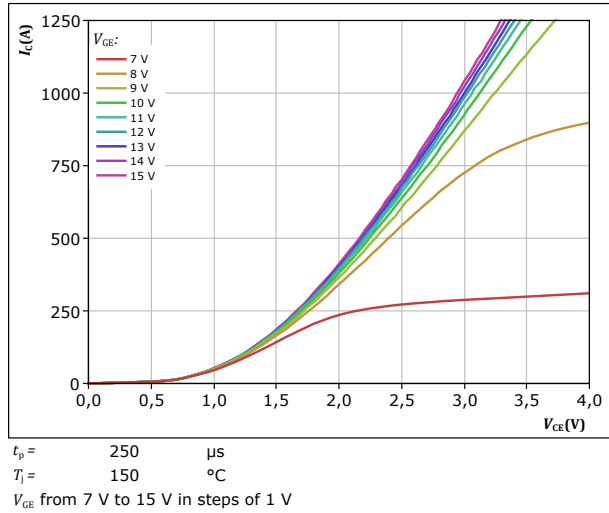


figure 11. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

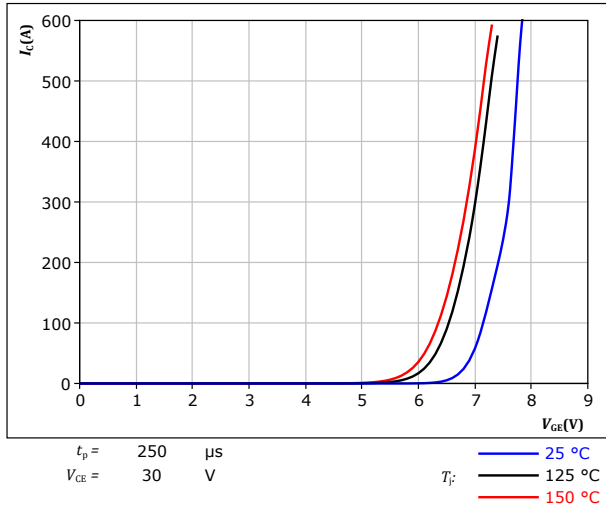
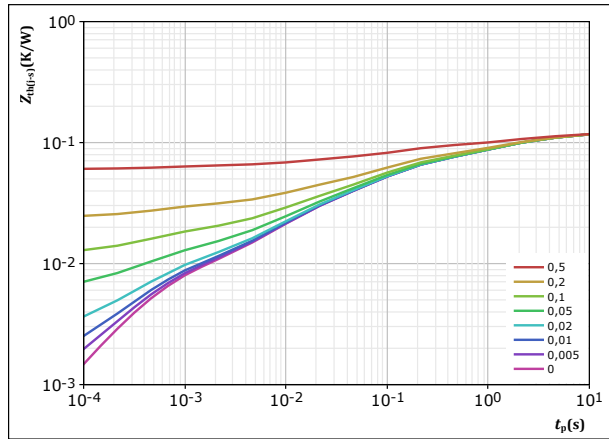


figure 12. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D =$	t_p / T	K/W
$R_{th(j-s)} =$	0,118	
IGBT thermal model values		
R (K/W)	τ (s)	
2,63E-02	4,95E+00	
3,21E-02	9,67E-01	
3,90E-02	9,38E-02	
1,61E-02	1,02E-02	
6,96E-03	4,96E-04	



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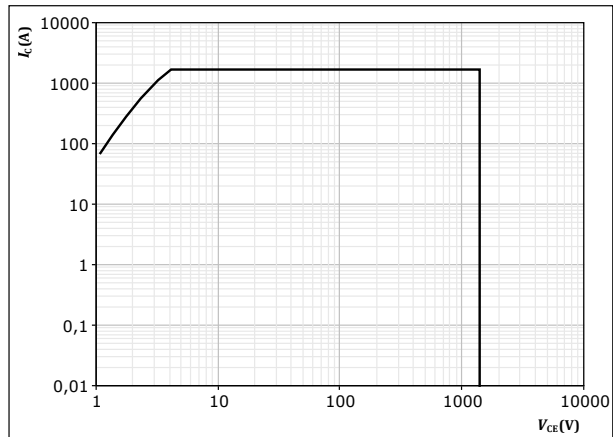
Boost Switch Characteristics

figure 13.

IGBT

Safe operating area

$$I_C = f(V_{CE})$$



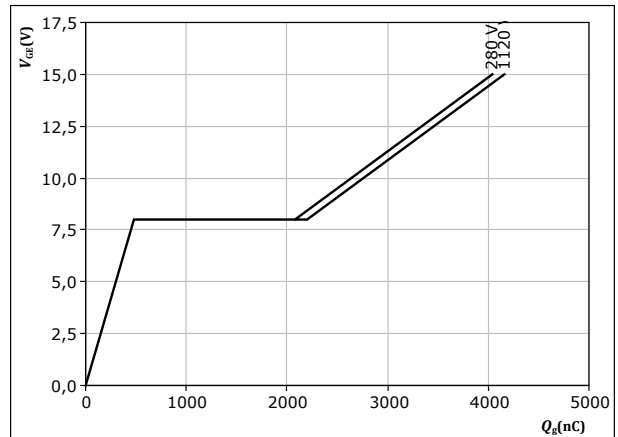
$D =$ single pulse
 $T_s = 80$ °C
 $V_{GE} = 15$ V
 $T_j = T_{jmax}$

figure 14.

IGBT

Gate voltage vs gate charge

$$V_{GE} = f(Q_g)$$



$I_C = 400$ A
 $T_j = 25$ °C



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Boost Diode Characteristics

figure 15.

FWD

Typical forward characteristics

$$I_F = f(V_F)$$

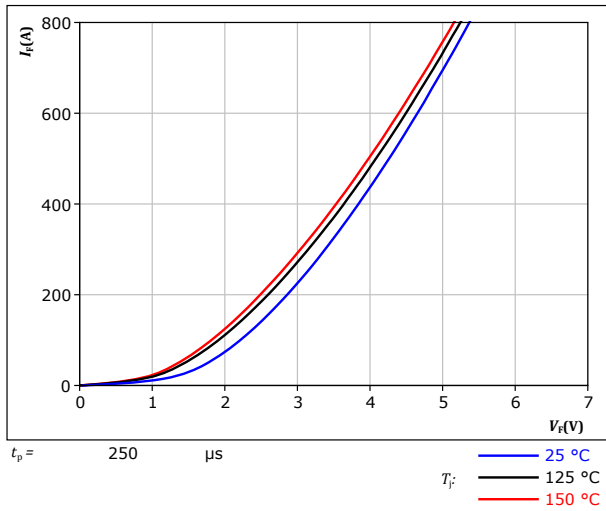
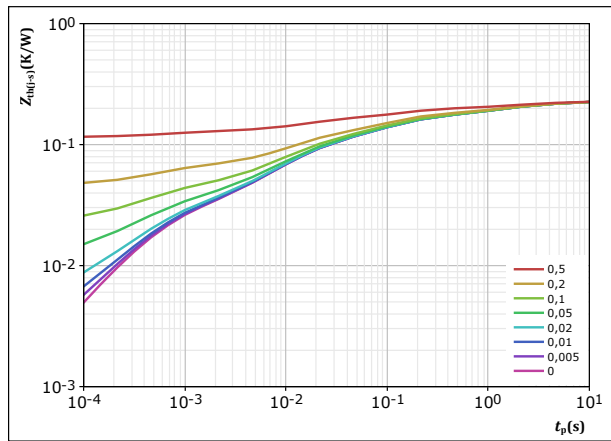


figure 16.

FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D =$	t_p / T	
$R_{th(j-s)} =$	0,226	K/W
FWD thermal model values		
R (K/W)	τ (s)	
2,33E-02	6,11E+00	
4,55E-02	1,17E+00	
7,42E-02	9,47E-02	
6,40E-02	1,10E-02	
2,26E-02	4,76E-04	



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Boost Sw. Inv. Diode Characteristics

figure 17.

FWD

Typical forward characteristics

$$I_F = f(V_F)$$

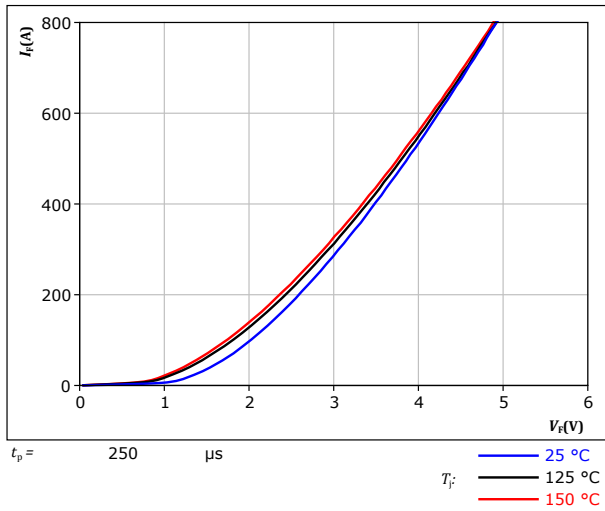
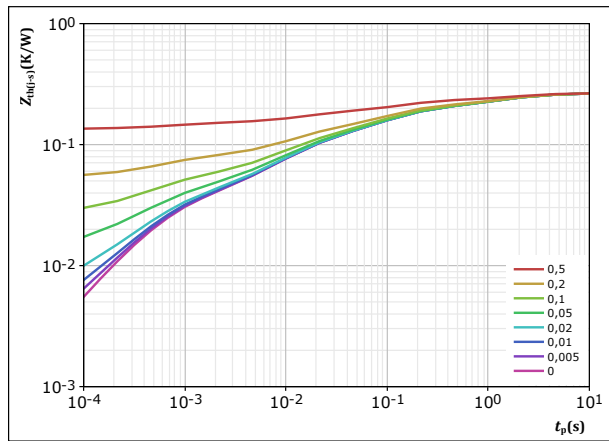


figure 18.

FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$





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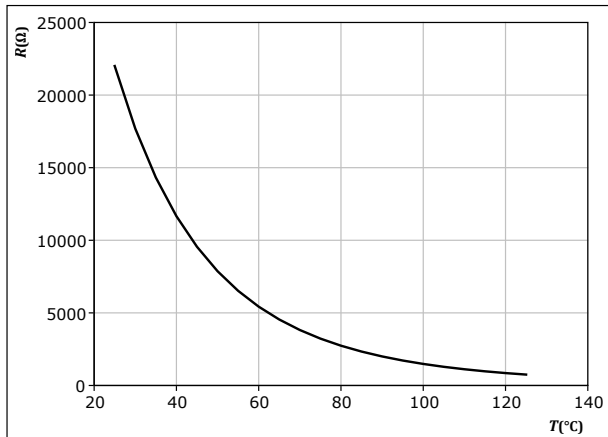
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Thermistor Characteristics

figure 19. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$





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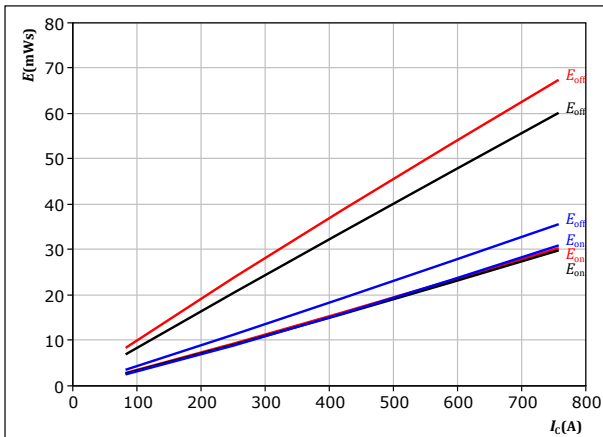
Buck Switching Characteristics

figure 20.

IGBT

Typical switching energy losses as a function of collector current

$$E = f(I_C)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

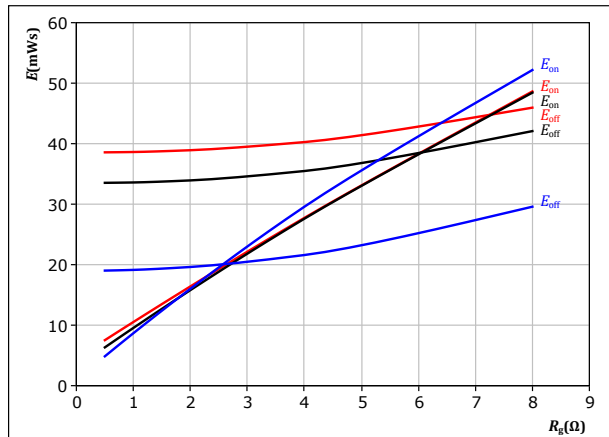
T_j : 25 °C
125 °C
150 °C

figure 21.

IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor

$$E = f(R_g)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

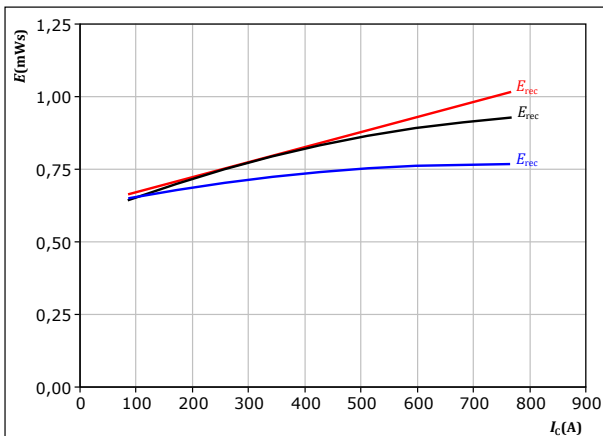
T_j : 25 °C
125 °C
150 °C

figure 22.

FWD

Typical reverse recovered energy loss as a function of collector current

$$E_{rec} = f(I_C)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

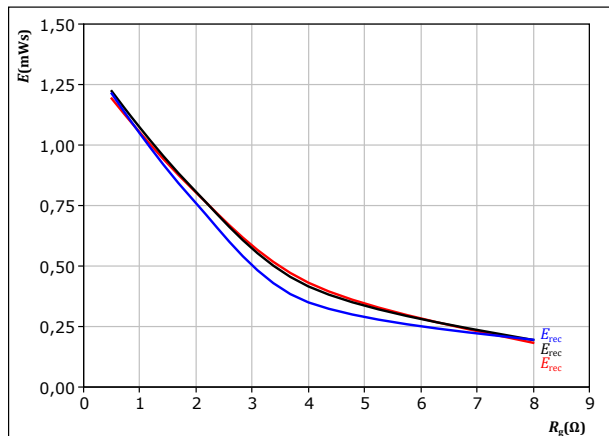
T_j : 25 °C
125 °C
150 °C

figure 23.

FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

T_j : 25 °C
125 °C
150 °C



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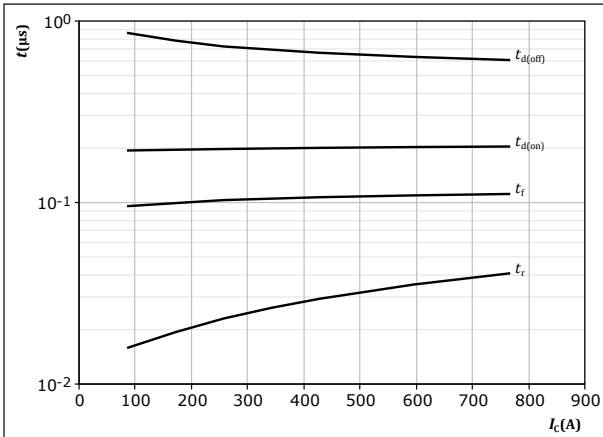
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datasheet

Buck Switching Characteristics

figure 24.

IGBT

Typical switching times as a function of collector current
 $t = f(I_C)$



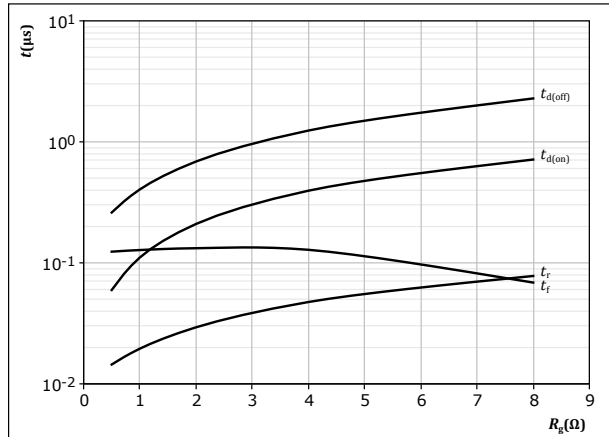
With an inductive load at

$T_j = 150$ °C
 $V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

figure 25.

IGBT

Typical switching times as a function of IGBT turn on gate resistor
 $t = f(R_g)$



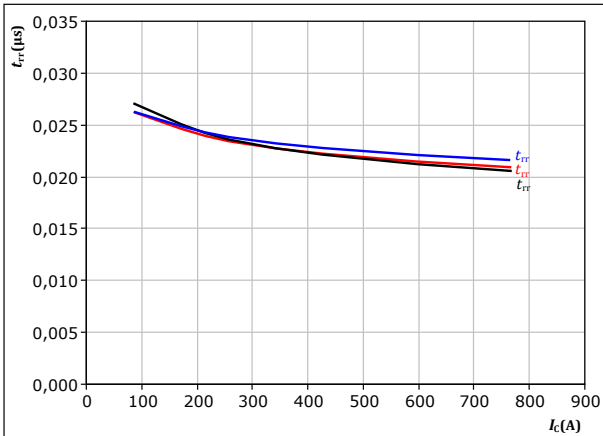
With an inductive load at

$T_j = 150$ °C
 $V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

figure 26.

FWD

Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_C)$



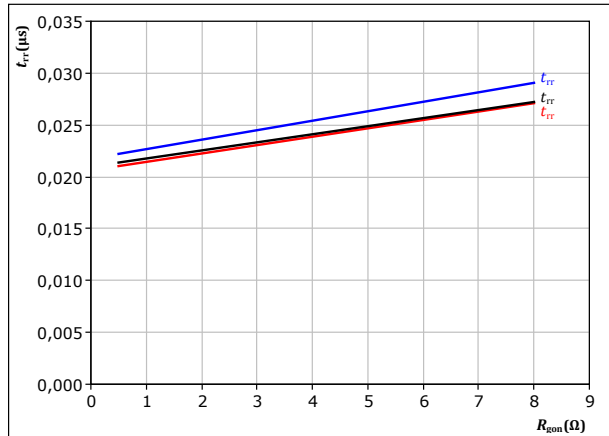
With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω
 $T_j: 25$ °C (blue), 125 °C (black), 150 °C (red)

figure 27.

FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A
 $T_j: 25$ °C (blue), 125 °C (black), 150 °C (red)



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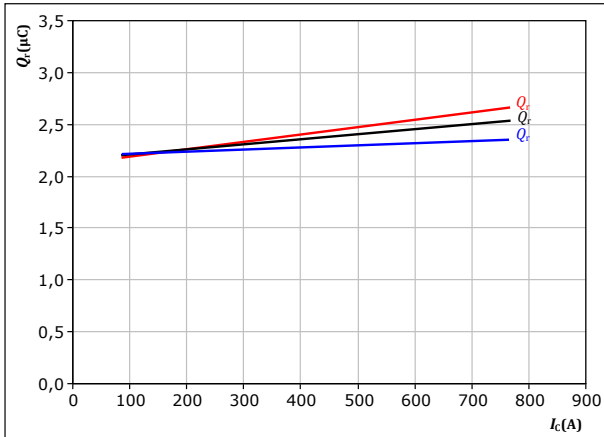
Buck Switching Characteristics

figure 28.

FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_C)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

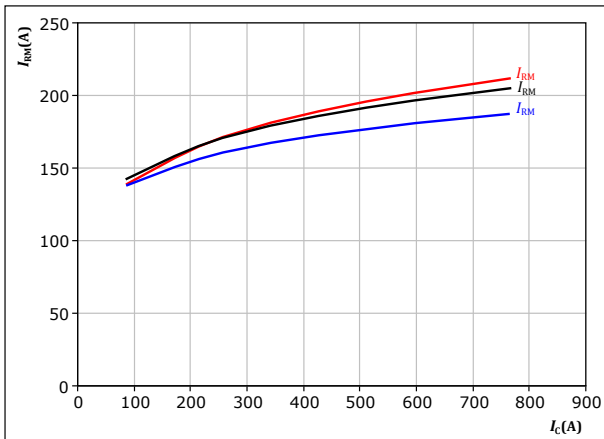
T_j : 25 °C
125 °C
150 °C

figure 30.

FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_C)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

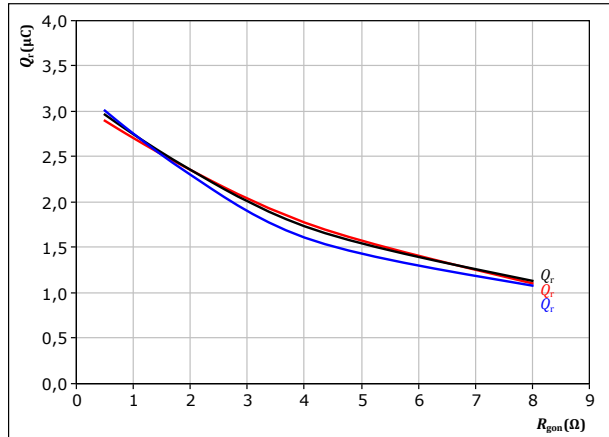
T_j : 25 °C
125 °C
150 °C

figure 29.

FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gon})$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

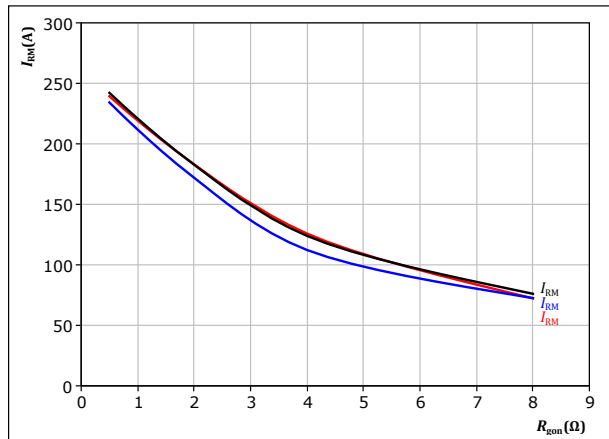
T_j : 25 °C
125 °C
150 °C

figure 31.

FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gon})$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

T_j : 25 °C
125 °C
150 °C



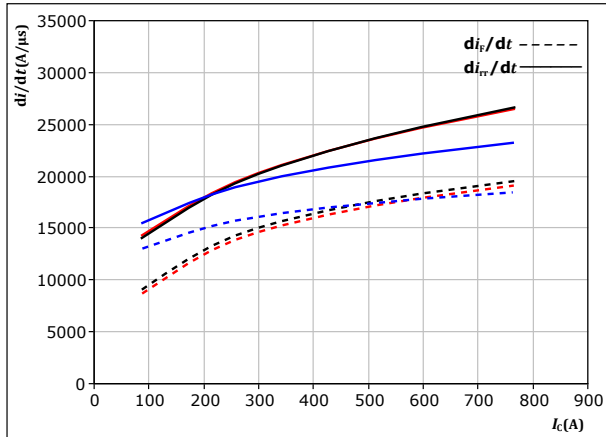
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datasheet

Buck Switching Characteristics

figure 32. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_C)$



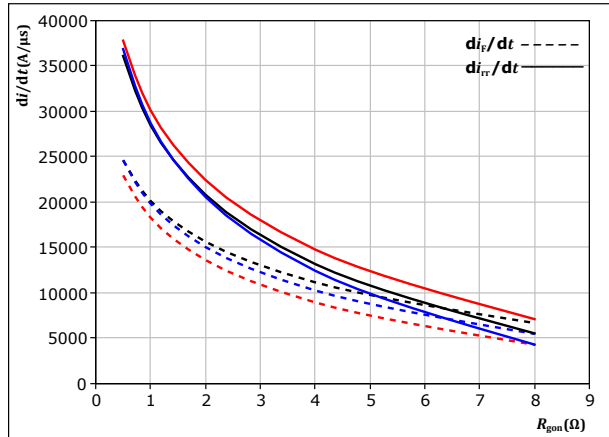
With an inductive load at

$V_{CE} = 800 \text{ V}$
 $V_{GE} = -5/15 \text{ V}$
 $R_{gon} = 2 \text{ } \Omega$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 33. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor
 $di_f/dt, di_r/dt = f(R_{gon})$



With an inductive load at

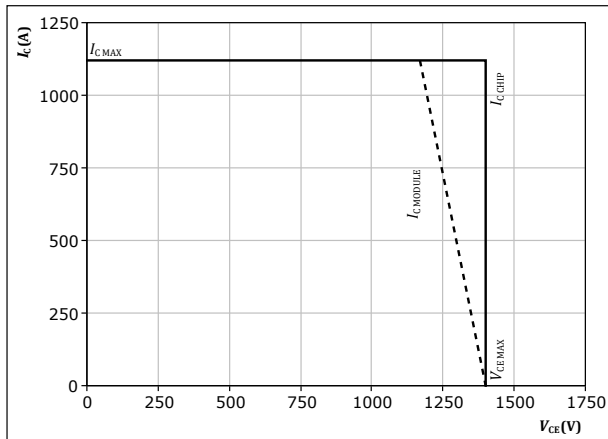
$V_{CE} = 800 \text{ V}$
 $V_{GE} = -5/15 \text{ V}$
 $I_C = 420 \text{ A}$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 34. IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



At $T_j = 150 \text{ } ^\circ\text{C}$
 $R_{gon} = 2 \text{ } \Omega$
 $R_{goff} = 2 \text{ } \Omega$



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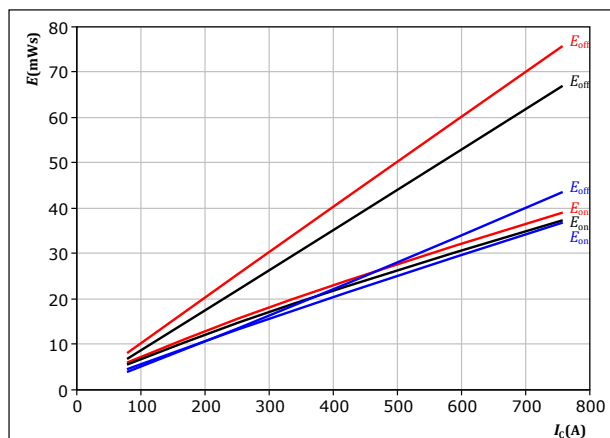
Boost Switching Characteristics

figure 35.

IGBT

Typical switching energy losses as a function of collector current

$$E = f(I_c)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

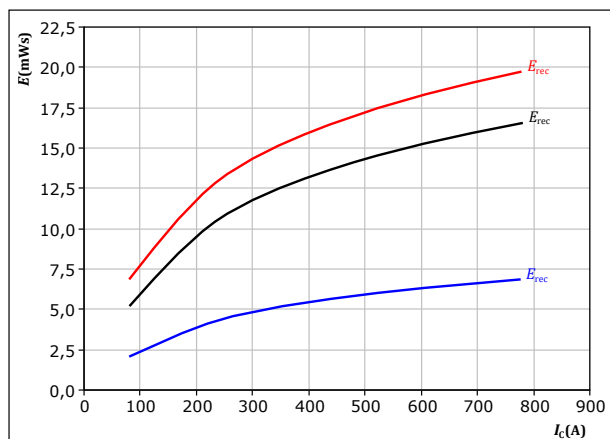
T_j : 25 °C
125 °C
150 °C

figure 37.

FWD

Typical reverse recovered energy loss as a function of collector current

$$E_{rec} = f(I_c)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

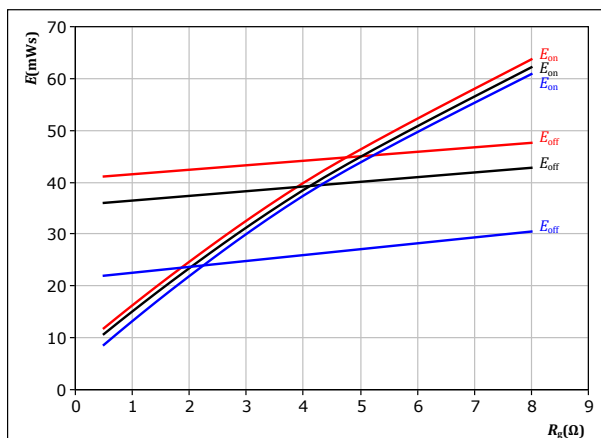
T_j : 25 °C
125 °C
150 °C

figure 36.

IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor

$$E = f(R_g)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_c = 420$ A

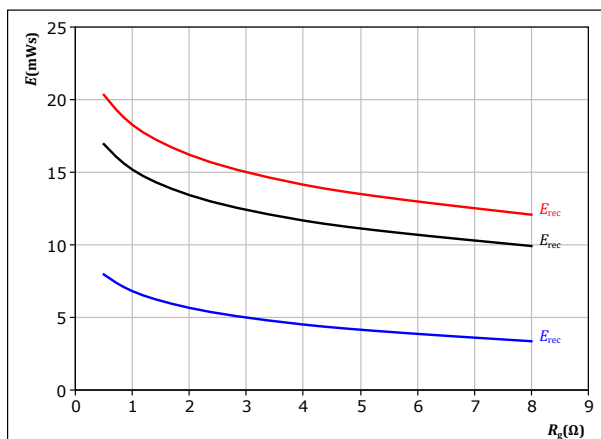
T_j : 25 °C
125 °C
150 °C

figure 38.

FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_c = 420$ A

T_j : 25 °C
125 °C
150 °C



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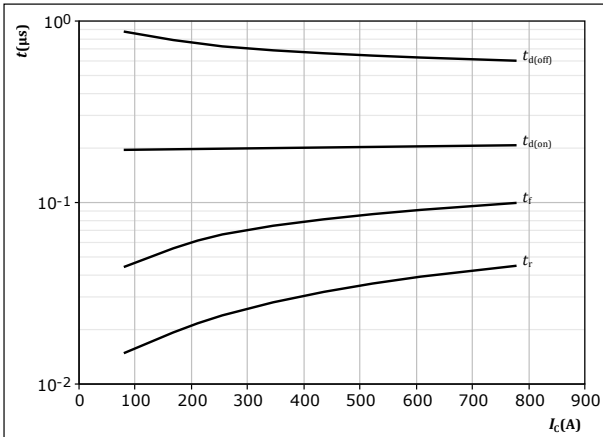
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Boost Switching Characteristics

figure 39.

IGBT

Typical switching times as a function of collector current
 $t = f(I_C)$



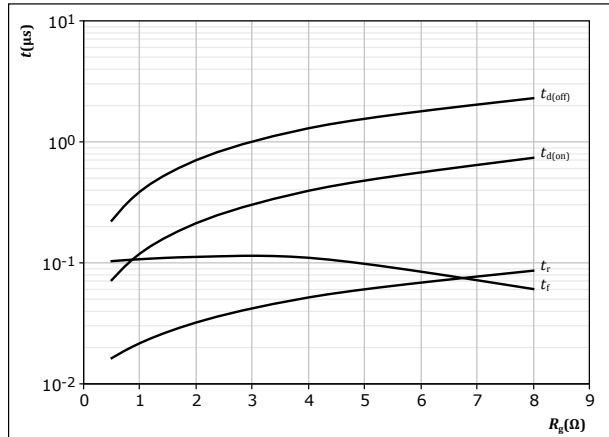
With an inductive load at

$T_j = 150$ °C
 $V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

figure 40.

IGBT

Typical switching times as a function of IGBT turn on gate resistor
 $t = f(R_g)$



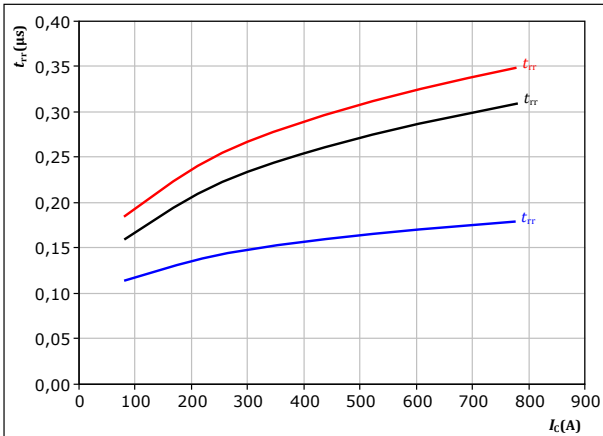
With an inductive load at

$T_j = 150$ °C
 $V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

figure 41.

FWD

Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_C)$



With an inductive load at

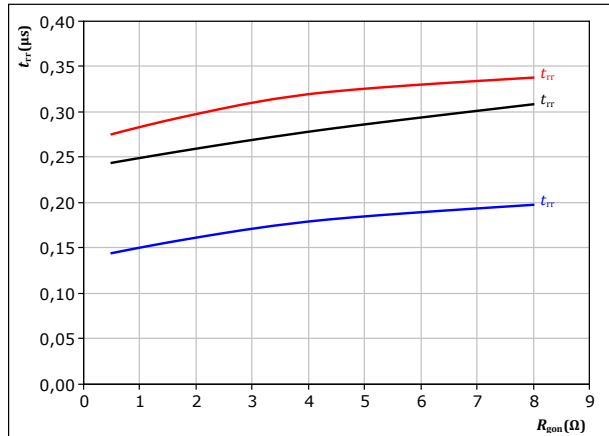
$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

T_j : 25 °C
125 °C
150 °C

figure 42.

FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_C = 420$ A

T_j : 25 °C
125 °C
150 °C



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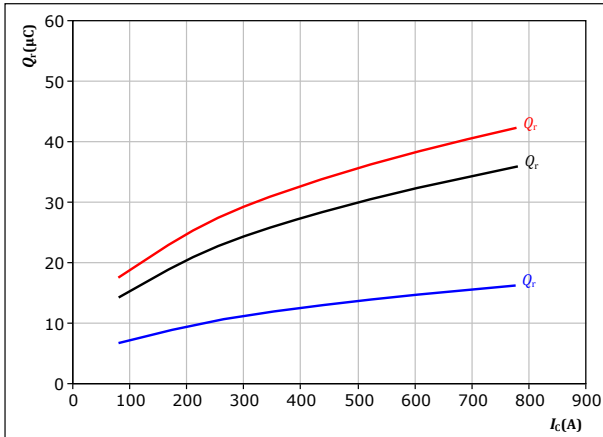
Boost Switching Characteristics

figure 43.

FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

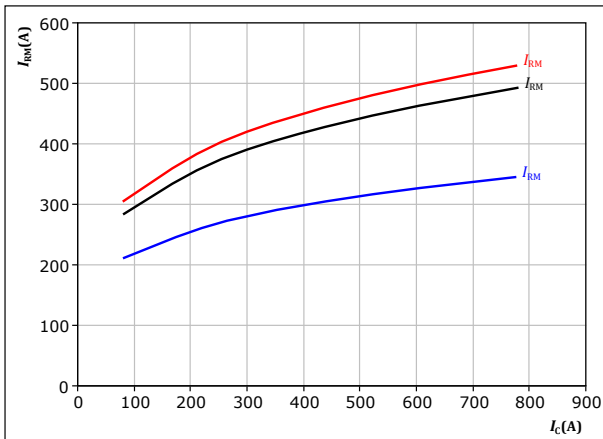
T_j : 25 °C
125 °C
150 °C

figure 45.

FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $R_{gon} = 2$ Ω

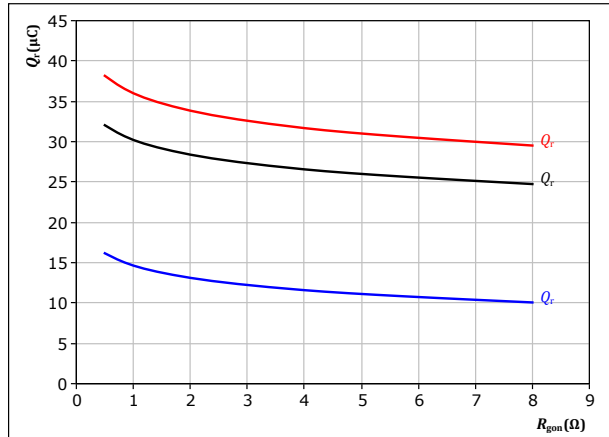
T_j : 25 °C
125 °C
150 °C

figure 44.

FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gon})$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_c = 420$ A

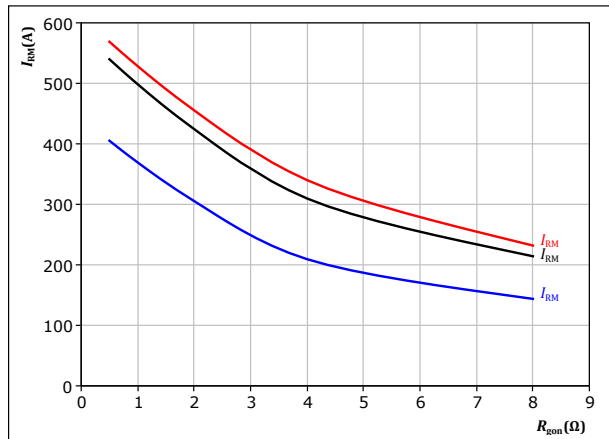
T_j : 25 °C
125 °C
150 °C

figure 46.

FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gon})$$



With an inductive load at

$V_{CE} = 800$ V
 $V_{GE} = -5/15$ V
 $I_c = 420$ A

T_j : 25 °C
125 °C
150 °C



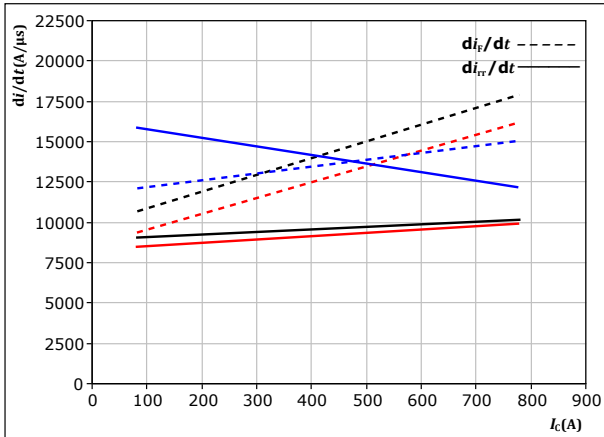
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datasheet

Boost Switching Characteristics

figure 47. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_C)$



With an inductive load at

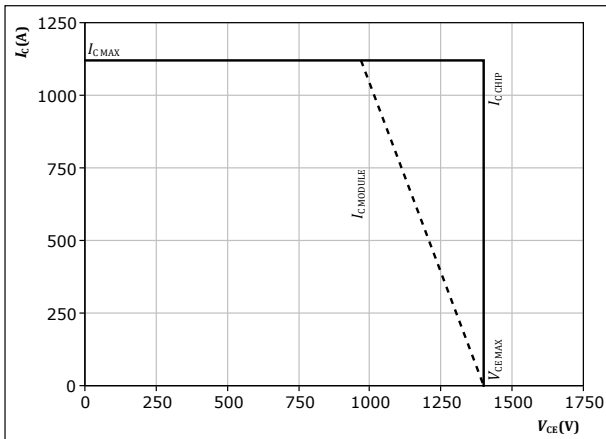
$V_{CE} = 800 \text{ V}$
 $V_{GE} = -5/15 \text{ V}$
 $R_{gon} = 2 \text{ } \Omega$

T_j : 25 °C
125 °C
150 °C

figure 49. IGBT

Reverse bias safe operating area

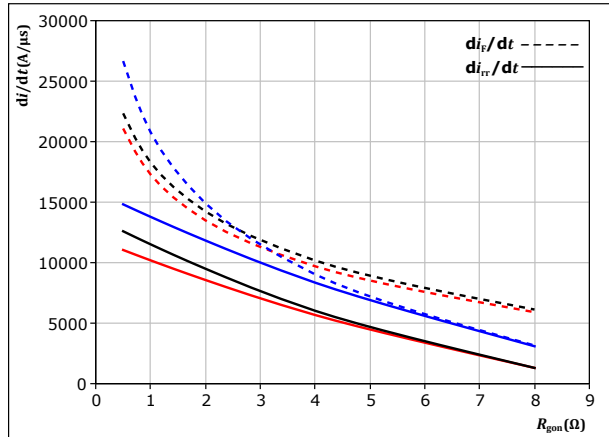
$I_C = f(V_{CE})$



At $T_j = 150 \text{ } ^\circ\text{C}$
 $R_{gon} = 2 \text{ } \Omega$
 $R_{goff} = 2 \text{ } \Omega$

figure 48. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor
 $di_f/dt, di_r/dt = f(R_{gon})$



With an inductive load at

$V_{CE} = 800 \text{ V}$
 $V_{GE} = -5/15 \text{ V}$
 $I_C = 420 \text{ A}$

T_j : 25 °C
125 °C
150 °C



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Switching Definitions

figure 50. IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})

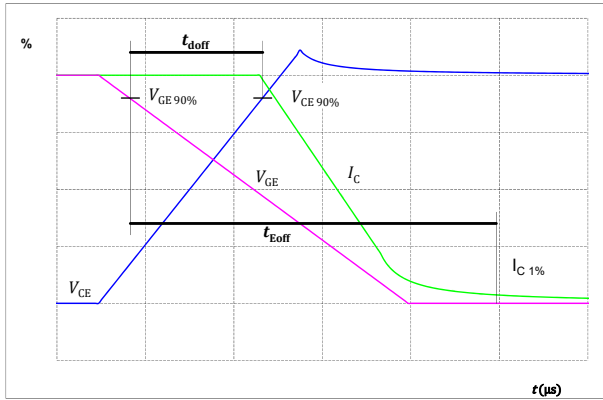


figure 51. IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})

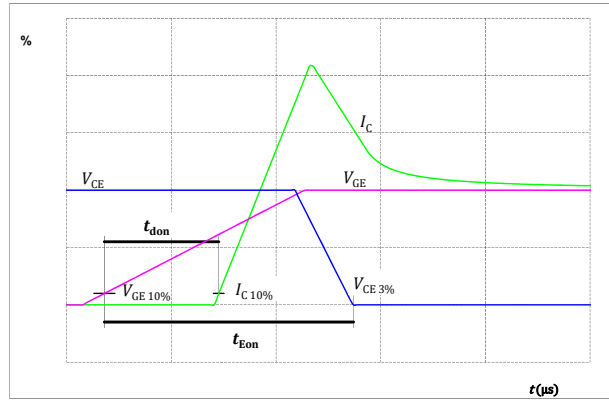


figure 52. IGBT

Turn-off Switching Waveforms & definition of t_f

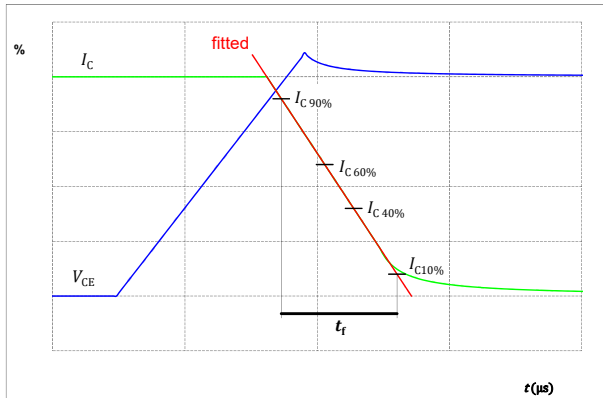
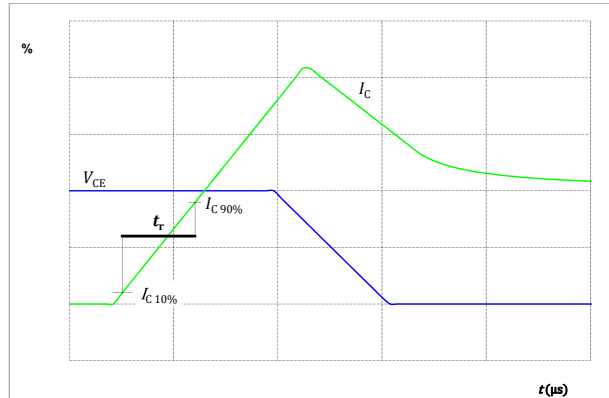


figure 53. IGBT

Turn-on Switching Waveforms & definition of t_r





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Switching Definitions

figure 54.

FWD

Turn-off Switching Waveforms & definition of t_{rr}

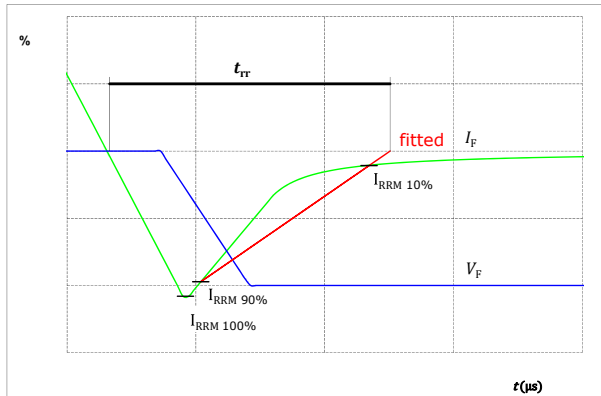
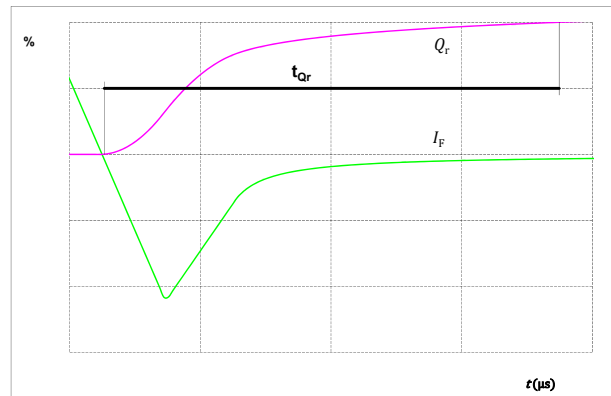


figure 55.

FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)

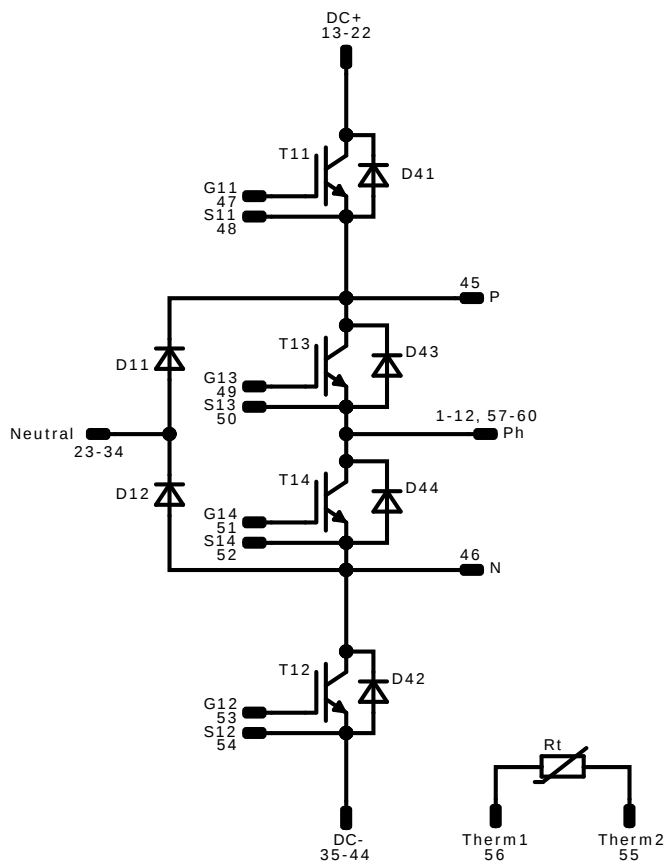




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Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12	IGBT	1400 V	560 A	Buck Switch	
D11, D12	FWD	1400 V	240 A	Buck Diode	
T13, T14	IGBT	1400 V	560 A	Boost Switch	
D42, D41	FWD	1400 V	300 A	Boost Diode	
D44, D43	FWD	1400 V	280 A	Boost Sw. Inv. Diode	
Rt	Thermistor			Thermistor	



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30-EQ14NIA560H701-PS59F86T
datasheet

Packaging instruction				
Standard packaging quantity (SPQ) 24	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow</i> E3BP packages see vincotech.com website.

Package data
Package data for <i>flow</i> E3BP packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
Certification pending. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
30-EQ14NIA560H701-PS59F86T-D3-14	4 Sep. 2026	Change Buck Diodes	
30-EQ14NIA560H701-PS59F86T-D4-14	22 Sep. 2026	Correct Housing Features	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.